

The Institution of Engineers, Malaysia (Penang Branch)

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BEM Approved
CPD/PDP Hours: 3
Ref No: IEM16/PG/306/V

Technical Visit to Silterra Malaysia Sdn Bhd

Date: 24 August 2016 (Wednesday)

Time : 1.45 pm - 4.45 pm

Venue : Lot 8, Industrial Zone Phase 2

Kulim Hi-tech Park 09000 Kulim, Kedah

Introduction

Silterra is a project of strategic national interest to promote front-end semiconductor manufacturing and a catalyst for high technology investments in Malaysia. It was founded in November 1995 as Wafer Technology Malaysia Sdn Bhd and was renamed as Silterra Malaysia Sdn Bhd in December 1999. Silterra broke ground on first manufacturing facility in Kulim, Malaysia in June 1999 and produced working silicon in November 2000. Since its inception, Silterra has served many top-tier global fabless design and product companies covering the consumer electronics, communications & computing, and mobile device market segments.

Silterra offers CMOS design and a broad range of fabrication processes for Integrated Chips (IC) in Advanced Logic, Mixed Signal & Radio Frequency and High Voltage applications. Silterra provides complete design solutions for customers to create leading-edge products, optimized for its high-yielding manufacturing processes, through strategic partnerships with industry-leading Intellectual Property (IP) design library providers, Design Services and Electronic Design Automation (EDA) suppliers. Silterra also offers comprehensive in-house Failure Analysis (FA) services to high-tech companies and universities, performing detailed construction and failure analysis of nano-scale structures.

Objective for this technical visit is to provide an opportunity to fellow engineers to gain exposure on foundry matched process and also witness Siterra the cornerstone for expanding semiconductor manufacturing services in Malaysia.

IEM Penang Branch is the administrator of the above activity

Biodata of Speaker



DR. MOHD. AZIZI BIN CHIK is currently the deputy director of industrial and manufacturing systems engineering in Silterra. Prior to this, he was a senior manager and member of technical staff-since 2008. He was a team member of Industrial Engineering for the SilTerra Fab start-up team in year 2000.

Graduated with a Bachelor of Science in industrial engineering from University of Missouri – Columbia, USA, in 1998 and attained a Master of Science in Electrical and Electronics from Universiti Kebangsaan Malaysia in 2006, Dr. Mohd Azizi was awarded a Doctorate in Nanoelectronics from Universiti Malaysia Perlis in 2015. He has more than 16 years of experience in the semiconductor manufacturing field specifically in manufacturing systems and industrial engineering.

Dr. Mohd Azizi is passionate and has very keen interest for continuous improvements in optimizing manufacturing operations and capacity planning. Based on solutions implemented in the factory, he has filed USPTO patents and trade secrets as well as written more than 30 publications of journals for national and international conferences in manufacturing systems integration, scheduling and capacity optimization.

In recognition of his knowledge and experience, he was given the opportunity to be involved in the PEMANDU Lab for E&E in 2010 and 2012 for country level planning and various presentations to the ministries.

Programme

1.45 pm - Arrival at Silterra Malaysia Sdn. Bhd & Registration

2.00pm - Overview of Silterra.

- Technical Presentation: "Semiconductor Fabrication in Silterra" by Dr. Mohd Azizi Bin Chik

- Silterra Fab Tour/ Facility visit

4.30 pm - Round-up/Photo Session

4.45 pm - End

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REGISTRATION FORM

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Moderator: Dr Huzein Fahmi bin Hawari

Registration: on a 'first-come first-served' basis. Closing Date: 17 August 2016 (Weds)

Limited: 25 pax only

Due to Silterra's request, visitors will be broken into 2 groups: (First 10 pax - cleanroom visit, 15 pax - facility visit)

REGISTRATION FEES						
Student	Graduate	IEM Member	Non-IEM Member	Senior IEM Member		
FOC	RM20	RM30	RM60	RM20		

Payment Mode (please tick where applicable)

- 1. Online bank-in to: AMBANK Acct#: 002-201-101022-6 (please e-mail/fax receipt to us with attendee (s) name on it
- 2. Crossed cheque in favour of: The Institution of Engineers, Malaysia
- 3. Cash to: The IEM Penang Secretariat

Closing Date: payment should reach us latest by 17 August 2016 (Weds)

We wish to register the following person(s) for this Technical Visit.

I/We understand that the fee is not refundable.

If I/we withdraw after my/our registration is accepted, substitution of participants will be allowed. If I/we fail to attend the event, the fee paid will not be refunded.

#	FULL NAME (s) in CAPITALS	NEW IC#	MOBILE#	IEM#	PE#	FEES (RM)
1						
2						

Name of Organisatio	n:		
Tel: (o)	Fax:	Email:	
Contact Person:		Designation:	
Signature:		Date:	

Note: IEM reserves the right to cancel the technical visit with full refund